

Notice of References Cited	Application/Control No. 09/842,082	Applicant(s)/Patent Under Reexamination LEE ET AL.	
	Examiner Usha Raman	Art Unit 2616	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,505,243	01-2003	Lortz, Victor	709/220
	B	US-6,629,134	09-2003	Hayward et al.	709/217
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP 06008594 A	01-1994	Japan	KANEKO, MASARU	B41J 29/46
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Real-Time Error Mapping of a Memory Basic Storage Module or System with No Performance Degradation to the System", IBM Technical Disclosure, Volume 28, page 1567, published in Sept. 1985
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.